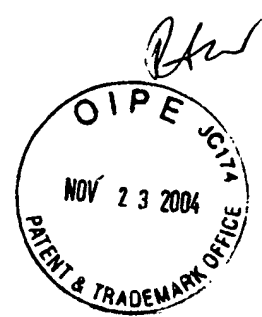


Docket No.: 60188-028

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277
Toshio YAMADA : Confirmation Number: 5852
Application No.: 09/779,751 : Group Art Unit: 2127
Filed: February 09, 2001 : Examiner: Lilian VO

For: SEMICONDUCTOR INTEGRATED CIRCUIT, COMPUTER SYSTEM, DATA PROCESSOR AND DATA PROCESSING METHOD

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Transmitted herewith is an Amendment in the above-identified application.

- ☒ No additional fee is required.
☐ Applicant is entitled to small entity status under 37 CFR 1.27
☐ Also attached:

The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	7	20	0	\$18.00 =	\$0.00
Independent Claims	1	6	0	\$88.00 =	\$0.00
Multiple claims newly presented					\$0.00
Fee for extension of time					\$0.00
					\$0.00
Total of Above Calculations					\$0.00

- ☐ Please charge my Deposit Account No. 500417 in the amount of \$0.00. An additional copy of this transmittal sheet is submitted herewith.
- ☒ The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

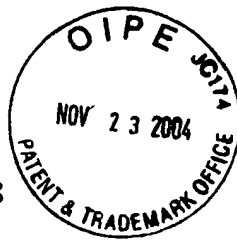
McDERMOTT WILL & EMERY LLP

Michael E. Fogarty
Registration No. 36,139

600 13th Street, N.W.
Washington, DC 20005-3096
Phone: 202.756.8000 MEF:jrj
Facsimile: 202.756.8087
Date: November 23, 2004

Please recognize our Customer No. 20277 as our correspondence address.

Docket No.: 60188-028



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	Customer Number: 20277
Toshio YAMADA	:	Confirmation Number: 5852
Application No.: 09/779,751	:	Group Art Unit: 2127
Filed: February 09, 2001	:	Examiner: Lilian VO

For: SEMICONDUCTOR INTEGRATED CIRCUIT, COMPUTER SYSTEM, DATA
PROCESSOR AND DATA PROCESSING METHOD

AMENDMENT UNDER 37 C.F.R. § 1.111

Mail Stop Non Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated August 23, 2004, having a shortened statutory period for response set to expire November 23, 2004, please amend the above-identified application as follows: